

Process Tomography of Quantum Memory in a Josephson Phase Qubit coupled to a Two-Level State

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A quantum computer will require quantum bits (qubits) with good coherence that can be coupled together to form logic gates^{1,2}. Superconducting circuits offer a novel solution³⁻⁹ since qubits can be connected in elaborate ways through simple wiring, much like that of conventional integrated circuits. However, this ease of coupling is offset by coherence times shorter than those observed in molecular and atomic systems. Hybrid architectures could help skirt this fundamental trade-off between coupling and coherence by using macroscopic qubits for coupling and atom-based qubits for coherent storage^{10,11}. Here we demonstrate the first quantum memory operation¹² on a Josephson phase qubit by transferring an arbitrary quantum state to an atomic two-level state (TLS)¹³, storing it there for some time, and later retrieving it. The qubit is used to probe the coherence of the TLS by measuring its energy relaxation and dephasing times. Quantum process tomography^{2,14} completely characterizes the memory operation, yielding an overall process fidelity of 79%. While the uncontrolled distribution of TLSs precludes their direct use in a scalable architecture, the ability to coherently couple a macroscopic device with an atomic-sized system motivates a search for designer molecules that could replace the TLS in future hybrid qubits.

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In quantum computation, coupling atomic qubits over macroscopic distances is a long-standing technological challenge. In ion-trap architectures, qubits are physically moved to regions where they can be positioned close to each other and coupled electrostatically¹⁵. Approaches based on cavity-QED eliminate the difficulty of moving atoms, but instead use a resonant electromagnetic cavity to couple over macroscopic distances via guided photons^{16,5,6}. Several recent proposals meet this challenge using additional novel approaches^{10,11,17,18}.

Superconducting wires are a natural medium for coupling between macroscopic and atomic states because currents and voltages obey quantum mechanics over length scales from macroscopic to atomic dimensions. At the macroscopic scale, the coupling remains coherent because superconductors have small dissipation. At the atomic scale, coupling is possible because the tunnel junction has a dielectric thickness ~ 2 nm that approaches atomic size. When an atom carrying a single elementary charge moves by one atomic bond length inside such a tunnel junction, it produces a substantial image charge in the junction electrodes, coupling the atomic-scale motion to the macroscopic degrees of freedom of the currents and voltages in the circuit. We thus have a natural hybrid system: the atomic state in the junction is a “memory” qubit capable of storing a quantum state, whereas the Josephson junction itself is a “register” qubit capable of general logic operations and able to be coupled to other qubits over macroscopic distances.

Our register qubit, a flux-biased Josephson phase qubit, is shown schematically in Fig. 1a. The qubit is a nonlinear inductor-capacitor resonator, whose resonant frequency can be tuned by varying the magnetic flux applied to the loop. The nonlinear resonator is well modelled at typical flux biases with a cubic potential, as depicted in Fig. 1b. The non-linearity breaks the degeneracy (equal spacing) between adjacent energy levels, so that the application of microwaves produces transitions between only

one pair of quantum states. Experiments are performed so that only the two lowest-lying states are occupied; these constitute the qubit states $|0\rangle$ and $|1\rangle$. The qubit is controlled by applying pulses of magnetic flux. A quasi-DC pulse adiabatically changes the energy difference between $|0\rangle$ and $|1\rangle$, and the resulting accumulation of phase is equivalent to a Z-axis rotation of the Bloch vector². A microwave pulse at the transition frequency coherently changes the occupation of the state, and corresponds to an X- or Y-axis rotation¹⁹.

Our memory qubit is an atomic two-level state (TLS) located inside the Josephson tunnel barrier, shown in Figure 1c. A TLS is understood to be an atom, or a small group of atoms, that tunnels between two lattice configurations²⁰. Such states are ubiquitous in amorphous materials, whose disordered structure does not fully constrain the constituent atoms. The electric dipole moment inferred from measurements of these states is consistent with an atom carrying net charge e moving by about one atomic bond length²¹. Because the barrier thickness is ~ 20 atoms, the induced charge on the electrodes from this motion is large, $\sim e/20$.

TLS defects have been considered deleterious, as they provide additional quantum states to which the qubit may couple in an uncontrolled manner. A dense bath of these states is equivalent to dielectric loss, which decreases the lifetime of the qubit²¹. Decoherence from TLS defects can be removed by decreasing their number; this has been accomplished by reducing the junction area while shunting it with an external low-loss capacitor²².

Here we demonstrate that a TLS can play a useful role as well. A previous experiment coupled a qubit to a TLS¹³, and theoretical work has suggested their use as memory elements¹². In the present device, individual TLS states are well-separated from each other in frequency, and the qubit lifetime is sufficiently long, that we can

perform precise gate operations between the register qubit and one TLS. The ground and excited energy eigenstates of this TLS, labelled $|g\rangle$ and $|e\rangle$, constitute the memory qubit. By adjusting the flux bias, the register and memory qubits are tuned into and out of resonance, effectively turning on and off their coupling. We have found that a TLS memory can have a reasonably long coherence time, and thus represents a good model system for future hybrid qubits.

The register qubit transition frequency $|0\rangle \rightarrow |1\rangle$ is first measured as a function of flux bias using spectroscopy, as shown in Fig. 2. The splitting at ~ 7.05 GHz is due to a TLS with coupling strength $S = 41$ MHz that will be used as the memory qubit. We characterize the register qubit at the off-resonance frequency of 6.75 GHz, and find coherence times from standard energy decay, Ramsey, and spin-echo sequences to be $T_1=400$ ns, $T_2=120$ ns, and $T_2^*=350$ ns, respectively. The measurement visibility is high, approximately 90%. This phase qubit has a coherence time T_1 that is four times longer than previously reported, due to the use of a new low-loss dielectric a-Si:H^{21,23} in the shunt capacitor.

To characterize the memory qubit, we first tune the register qubit off-resonance to 6.75 GHz and excite it into the $|1\rangle$ state with a 16 ns long X_π -pulse. Then, a Z-pulse with 2 ns rise time moves the register qubit adiabatically into resonance with the memory qubit, effectively turning on the coupling. After waiting for time t , the register qubit state is measured. The resulting oscillations between the register and memory qubits, shown in Fig. 3a, have a 25 ns period that agrees with the coupling strength measured spectroscopically. In the rotating frame, the coupling is of the form¹² $(S/2)(\sigma_x\sigma_x + \sigma_y\sigma_y)$, so that the first minimum at 12 ns corresponds to an iSWAP gate²⁴, which takes $|1g\rangle \rightarrow i|0e\rangle$ and $|0e\rangle \rightarrow i|1g\rangle$. The envelope of the oscillations between the register and memory decays more slowly than for the register qubit alone, indicating that the memory qubit has a longer lifetime.

The coherence of the memory qubit is directly measured using two iSWAP operations, as shown in Fig. 3b and c. We start by exciting the register qubit off-resonance, and then move it into resonance with the memory qubit for time $t_{\text{swap}}=12$ ns to achieve state transfer into memory. After the register qubit is moved out of resonance to its starting frequency for a variable wait time t , it is then subjected to another iSWAP operation before measurement. Figure 3b shows a plot of the measurement probability versus wait time; the exponential decay gives a qubit memory time $T_1=1.2$ μs . Storing the quantum state in memory instead of the register increases its lifetime by a factor of three in our system, although this does not improve on the best T_1 times reported in other superconducting qubits. TLS lifetimes estimated from phonon radiation²⁵ depend on the electron-phonon coupling constant that varies greatly from defect to defect. The measured lifetime is consistent with typical predictions that fall in the range of 10 ns to 10 μs .

A similar Ramsey fringe experiment is used to measure dephasing, as shown in Fig. 3c. We first prepare the register qubit in the superposition $(|0\rangle + i|1\rangle)/\sqrt{2}$ with an $X_{\pi/2}$ -pulse, perform the same iSWAP / hold / iSWAP sequence as before, and then execute a final $\pi/2$ -pulse with swept phase. The envelope of the Ramsey oscillations indicates a memory dephasing time $T_2=210$ ns.

This iSWAP / hold / iSWAP sequence is in fact a quantum memory operation for an arbitrary initial state in the register qubit. The first iSWAP transfers the state to the memory qubit, where it is protected from decoherence during the hold time; the second iSWAP then restores the register qubit to its initial state (up to a correctable Z-rotation). We characterize this memory operation using Quantum Process Tomography (QPT)^{2,14}, which involves preparing a spanning set of input states, performing the quantum operation, and measuring the output with Quantum State Tomography (QST)²². The measured input and output states allow us to fully reconstruct the quantum memory

process. The control sequence for QPT is similar to that for Ramsey fringes, with the microwave pulses generalized to create the initial states and to perform QST on the final states.

QST is performed at three separate stages in the sequence, as shown in Figures 4a, 4b, and 4c. In 4b, after transfer to the memory qubit, the register qubit contains little amplitude of the initial state, as expected. The coherence of the memory operation is determined by comparing Fig. 4a to 4c, which shows only a small reduction in length of the Bloch vectors. Here we have compensated for the Z-rotation arising from the two iSWAPs and the 295 MHz detuning between the coupling “on” and “off” frequencies.

QPT gives us the χ -matrix of the memory operation², shown in Figure 4d. In this representation, the quantum operation acts on the input density matrix as $E(\rho) = \sum_{m,n} \chi_{mn} \hat{A}_m \rho \hat{A}_n^\dagger$, where $\{\hat{A}_m\}$ is some fixed set of basis operators, in our case the identity (I) and Pauli σ -matrices. Diagonalizing the χ -matrix leads to the operator sum representation (OSR), which we write as $E(\rho) = \sum_k w_k \hat{E}_k \rho \hat{E}_k^\dagger$, where the operation elements $\{\hat{E}_k\}$ are linear combinations of the basis operators, and the weights $\{w_k\}$ give the probabilities of applying each operation element. Table 1 shows the OSR of the memory operation, giving the weight w_k and the coefficients of the basis operators for each operation element \hat{E}_k . The dominant operation element is a near-identity, as we expect for a memory operation. The next-most dominant elements are primarily σ_z -like (T₂ dephasing) and σ_x - and σ_y -like (T₁ relaxation). The relative weights of these dephasing and relaxation elements are roughly as expected from the measured T₁ and T₂ times of the memory qubit, accounting for the overall length of the experiment ~40 ns. The simplest measure of fidelity, the trace overlap, gives a process fidelity of 79%.

Errors in the memory operation can be divided into several categories, including tomography errors during state preparation and measurement, storage errors during the

memory hold time, and transfer errors during the iSWAPs. Tomography errors will be reduced by ongoing work to improve single-qubit performance through, for example, better materials and microwave pulse-shaping. Storage errors represent the intrinsic T_1 and T_2 of the TLS memory qubit, and it may be possible to improve them by substituting a suitably engineered molecule. Finally, transfer errors that come from the register-memory interaction may be more difficult to reduce, although careful control of the qubit frequency in turning on and off the coupling should improve the transfer fidelity. Note that the transfer errors are independent of the memory hold time. Thus, after some crossover time t_c , the longer T_1 of the memory qubit offsets the transfer error, resulting in better overall memory fidelity than the register qubit alone. In this case, our analysis of the process tomography indicates that the TLS memory is useful beyond the crossover time $t_c \sim 100$ ns.

Although a TLS was suitable for this initial proof-of-principle demonstration of quantum memory, their use in a quantum computer is unlikely because of their intrinsically random nature and limited coherence time. However, this experiment explicitly demonstrates a bridge between macroscopic and atomic-sized qubit states in a Josephson qubit, which motivates a search for a properly engineered atomic-scale memory qubit. Such a molecule should have a transition frequency in the 3-15 GHz microwave range, a large electric dipole moment to couple to the capacitor, but a small motional dipole moment to minimize coupling to and loss from phonon radiation. With the increasing ability in the field of molecular electronics to fabricate designer molecules, a hybrid Josephson qubit with long coherence may be within reach.

Methods

Fabrication. The phase qubit used in this experiment was fabricated using procedures similar to those described in Ref. 22. We replaced the silicon nitride dielectric of that design with a new low-loss dielectric made from hydrogenated amorphous silicon to achieve ~ 4 times longer coherence time.

Qubit Control. Control signals for performing qubit manipulations are generated using a custom 2-channel 14-bit D/A converter with 1 ns waveform resolution. The D/A output is filtered with dissipative Gaussian filters. Quasi-DC pulses for Z-rotations and measurement are sent directly to the qubit, while the microwave control signals are sent to an IQ mixer to modulate the two quadratures of a microwave signal from a continuous-wave source. The input microwave frequency is set 100 MHz above the qubit frequency, and a sideband modulation is used to mix this signal into resonance with the qubit. This prevents leakage at the carrier frequency from causing qubit transitions, thereby increasing the on/off ratio of the microwave control.

State Tomography. Three suitably chosen measurements are sufficient to completely characterize a single-qubit state, for example, projections along the X-, Y- and Z-axes of the Bloch sphere. The phase qubit can only be measured along the Z-axis (distinguishing $|0\rangle$ and $|1\rangle$), but rotations can be applied before measurement to effectively measure along other axes. For the present device, the visibility of the $|0\rangle$ and $|1\rangle$ states is different, that is, the probability of correctly identifying $|0\rangle$ is not equal to the probability of correctly identifying $|1\rangle$. For this reason, we performed 6 measurements, one in each direction along the X-, Y- and Z-axes. These 6 measurements can be combined to yield the Bloch vector of the state.

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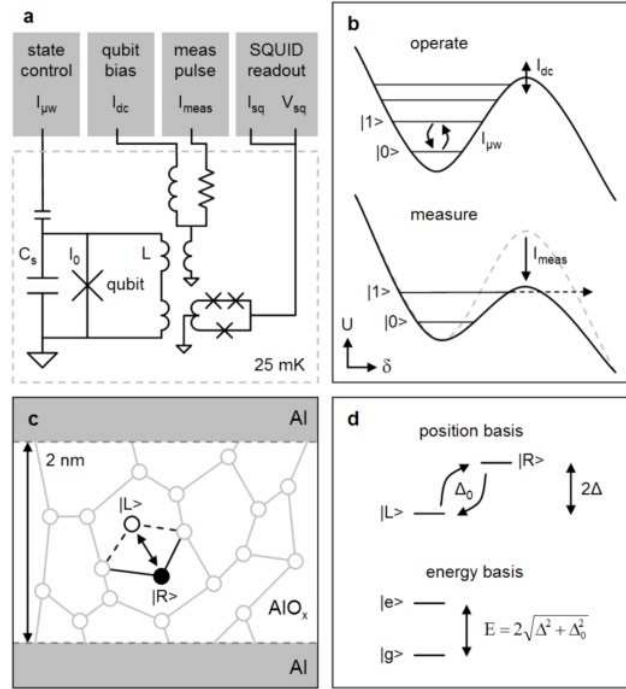


Figure 1 Register and Memory Qubits. **a**, Schematic of the flux-biased phase qubit (register), a nonlinear resonator with $L = 720$ pH, shunt capacitance $C_s = 1$ pF and junction critical current $I_0 = 2 \mu A$. **b**, Plot of potential energy versus superconducting phase difference across the Josephson junction. The potential energy $U(\delta)$ has a cubic shape, and the qubit states $|0\rangle$ and $|1\rangle$ are the two lowest-lying quantum states in the potential well. During operation, microwaves $I_{\mu w}$ (X- and Y-rotations) and quasi-DC pulses I_{dc} (Z-rotations) manipulate the qubit state. During measurement, a rapid (~ 3 ns) I_{meas} pulse is applied to lower the potential barrier, allowing only the 1-state to tunnel out of the well. **c**, Representation of a TLS (memory qubit) in the amorphous AlO_x of the Josephson junction. Because the barrier material is disordered, some atoms can occupy two positions, labelled $|L\rangle$ and $|R\rangle$. As shown in **(d)**, these positional states are separated by an energy difference 2Δ and connected by a tunnelling energy Δ_0 . The energy eigenstates, the ground state $|g\rangle$ and excited state $|e\rangle$, are separated by an energy E . The dipole moment associated with

charge motion between $|L\rangle$ and $|R\rangle$ couples to the currents and voltages in the qubit circuit.

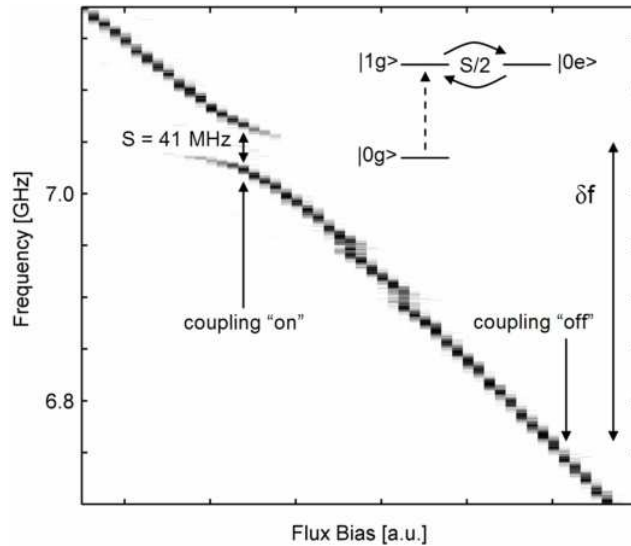


Figure 2 Modulation of the register-memory coupling. Spectroscopy plot showing register (phase) qubit excitation probability for microwave frequency versus flux bias. Probability P_1 of excitation to the $|1\rangle$ state is coded in gray-scale. Changing the flux bias tunes the qubit frequency over a range ~ 2 GHz. Coupling to the memory qubit (TLS) creates a splitting in the spectrum at 7.05 GHz. When on resonance, this coupling connects the register qubit excited state $|1g\rangle$ and the memory qubit excited state $|0e\rangle$, with a swap frequency equal to the splitting $S = 41$ MHz. When the register qubit is detuned from the memory qubit by δf , the coupling strength is reduced by $S^2/4\delta f^2$, effectively turning off the coupling.

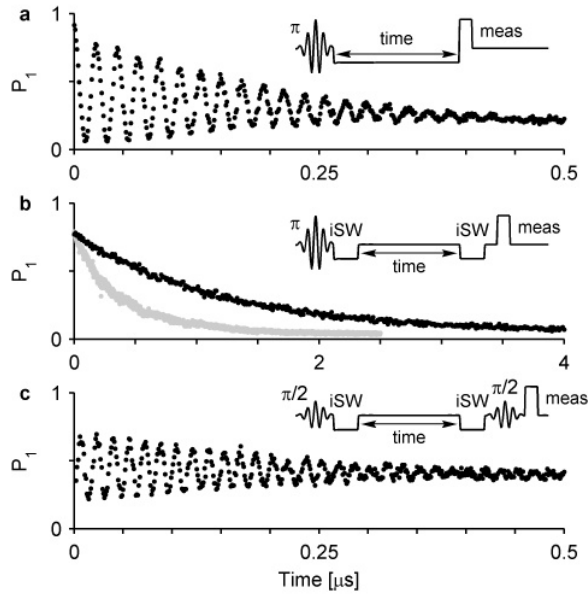


Figure 3 Time-domain interaction of the register and memory qubits. a-c, Register qubit excitation probability P_1 versus time for three pulse sequences, as represented in the insets. **a**, Free oscillations due to resonant coupling. The register qubit is excited with a pi-pulse while detuned from the memory qubit, and then brought into resonance for a variable time before being measured. The excitation probability oscillates as the state swaps between $|1g\rangle$ and $|0e\rangle$. The position of the first minimum gives an iSWAP time of 12 ns, in agreement with the spectroscopically measured splitting. The maximum probability decays to zero due to T_1 processes in both qubits. **b**, Energy decay of the memory qubit. The register qubit is excited while off-resonance, and then coupled to the memory qubit for 12ns to transfer the state with an iSWAP. A second iSWAP later restores the memory state into the register. The resulting decay (black) gives the lifetime of the memory element $T_{1,\text{mem}} = 1.2 \mu\text{s}$, which is longer than the register qubit lifetime $T_{1,\text{reg}} = 0.4 \mu\text{s}$ (gray). **c**, Ramsey fringes of the memory qubit. A superposition state $(|0\rangle + i|1\rangle)/\sqrt{2}$ is prepared in the register with a $\pi/2$ -pulse and transferred to memory. After variable time, the state is swapped back into the register and another $\pi/2$ -pulse is applied. The phase of

the second pulse is swept with time, leading to Ramsey interference fringes that decay due to dephasing of the state while in memory. The decay envelope gives $T_{2,\text{mem}} = 210$ ns.

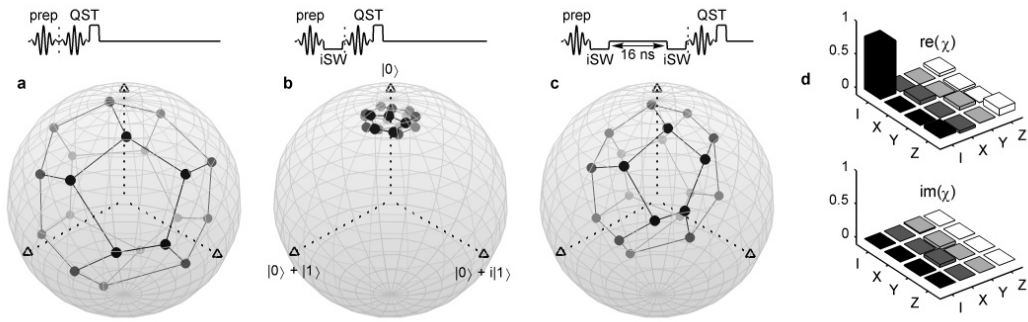


Figure 4 Process tomography of quantum memory. **a-c**, Register qubit state at three stages of the memory operation, using Quantum State Tomography (QST) to reconstruct the states on the Bloch sphere. Dots indicate the states, while edges have been added to highlight the structure. Insets show the pulse sequences used at each stage. **a**, Initial states lying at the vertices of a dodecahedron on the Bloch sphere. **b**, After the first iSWAP, the state has been transferred into the memory, and the register is left nearly in its ground state. **c**, After the second iSWAP, the state has been restored from memory into the register qubit. The structure of initial states is clearly preserved, though the amplitude has decreased due to T_1 and T_2 decay processes in the memory during the 16 ns hold time. **d**, Real and imaginary parts of the χ matrix for the memory operation, determined by comparing the initial states from **a** with the final states from **c**. The large I-I element indicates that the memory operation is a near-identity, as expected. From χ , we compute a process fidelity of 79%.

Table 1 Operator-sum representation of memory operation

Weight	I	σ_x	σ_y	σ_z
79.15%	0.9976	-0.0052 + 0.0007i	-0.0010 - 0.0213i	0.0653 + 0.0034i
12.92%	-0.0576 - 0.0002i	-0.5925 - 0.0451i	0.0797 - 0.3164i	0.7329
7.73%	0.0089 + 0.0236i	0.0109 - 0.4065i	0.6685	-0.0882 - 0.6160i
0.20%	-0.0281 - 0.0010i	0.6940	0.0370 - 0.6670i	0.2668 + 0.0138i